

PTO/SB/21 (09-04)

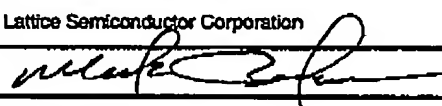
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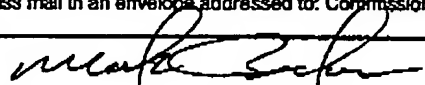
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/006,559	RECEIVED CENTRAL FAX CENTER OCT 25 2005
	Filing Date	12/03/2001	
	First Named Inventor	Antony Davies	
	Art Unit	2637	
	Examiner Name	Qutbuddin Ghulamali	
Total Number of Pages in This Submission	14	Attorney Docket Number	01-62

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	Lattice Semiconductor Corporation		
Signature			
Printed name	Mark L. Becker		
Date	10/25/05	Reg. No.	31325

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**First Named Inventor: Antony Davies****Application No.: 10/006,559****Filed: 12/03/2001****For PHASE SELECTION UNIT WITHIN PHASE LOCK
LOOP WITH MINIMIZED NUMBER OF PHASE
INTERPOLATORS****Examiner: Qutbuddin Ghulamali****Art Unit: 2637****Attorney Docket No. 01-52****RECEIVED
CENTRAL FAX CENTER****OCT 25 2005****RESPONSE AFTER FINAL ACTION****Applicants respond to the final Office action mailed 8/23/2005 as follows.**